

Fig. 3

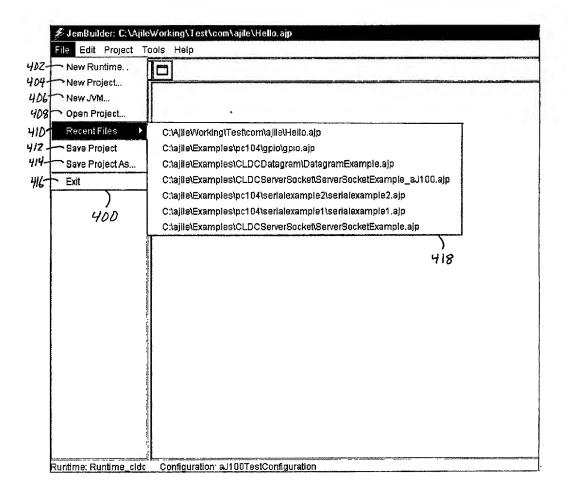


Fig. 4

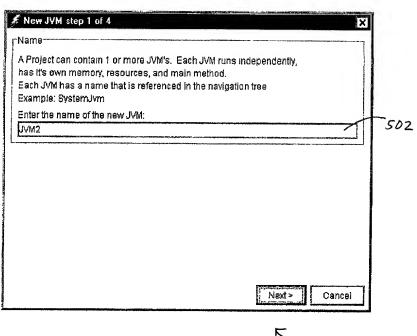
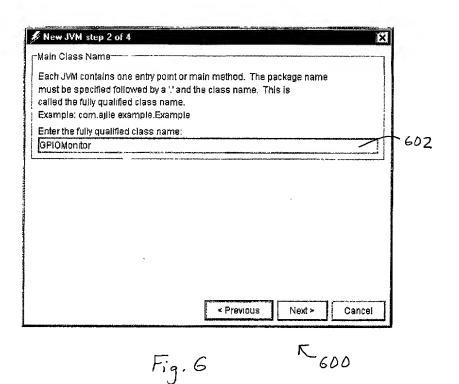
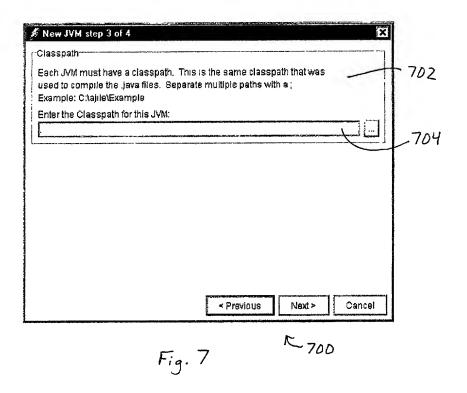
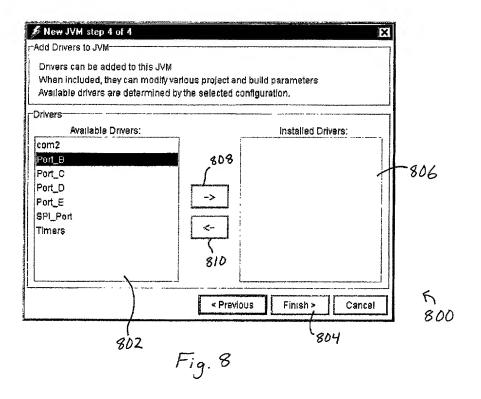


Fig. 5







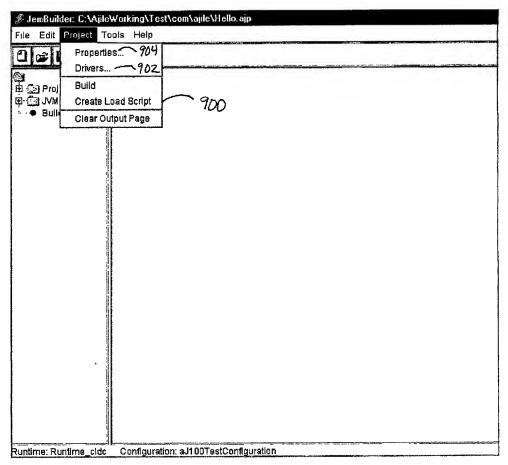
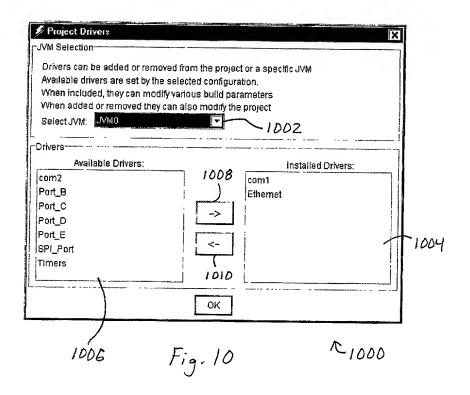
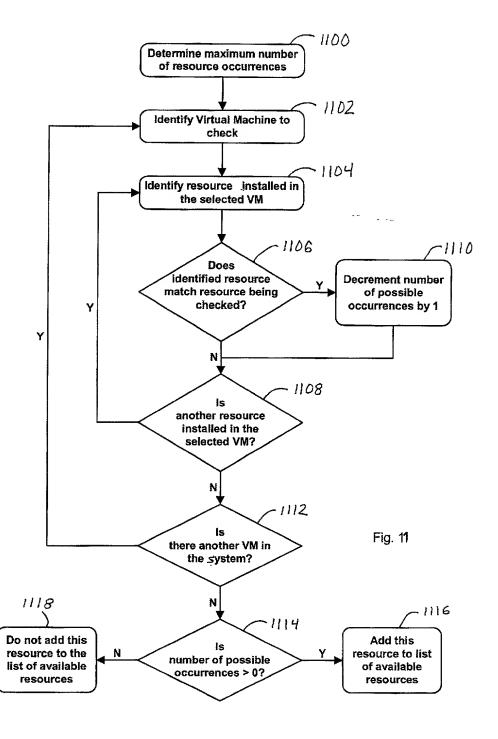
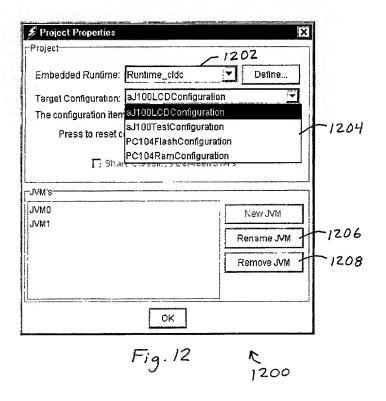


Fig.9







File Edit Project Tools Help  Project  Output Files  Output Files  PLL Setup  PIn Setup1  Pin Setup2  Chip Selects  Chip Selects  Main  Classpath  File Edit Project Tools Help  Physical Memory  RAM Start Address: 0x0  Default  RAM Size (bytes): 0x100000  1306  ROM Start Address: 0x0  1310
Project  Output Files  Memory   302   PLL Setup   Pin Setup1 Pin Setup2 Chip Selects   Main   Pchysical Memory   Physical Memory   Physical Memory   Physical Memory   Physical Memory   Default   D
Project
Memory Properties Listing Optimizations AJ100 Interrupts Nore Classes More Classes Memory Properties Listing Optimizations AJ100 Interrupts Divers  Divers D

Fig. 13

📕 JemBuilder: C:\local\Se	1-
File Edit Project Tools	
Project Output Files Mamory Pil Setup Pin Setup2 Chip Selects Classpath Memory Properties Listing Optimizations Al 100 interrupts invokes Traps Patch Classes More Classes More Classpath Memory Properties Listing Optimizations JVM1 Main Classpath Memory Properties Listing Optimizations JIVM1 Main Classpath Memory North Me	Phased Locked Loop Enable  Phased Locked Loop Enable  Phased Locked Loop Enable  Phased PLL operation  The lock timeout can be used to bypass the PLL for a number of clock cycles, until the PLL stabilizes. The Input clock will be used for the specified number of input clocks, then a switch is made to the PLL output. If the lock timeout is disabled, the PLL output will be used all of the time  3,670,016  Number of Cycles to PLL Driven Clock  Clock Frequency Calculation  1404  Clock Frequency Calculation  1405  10  Select the PLL Multipiller  1406  10  Select the PLL Divider  1410  Internal Clock Frequency in Hz  1412  As Internal Time Prescaler  Clock Out pin can be disabled, or its frequency divided down  2  Select the Clock Output Divider  1416  1416
recambe. Remains_stac Co	ringaration, actions occurring elation
1402	Fig. 14

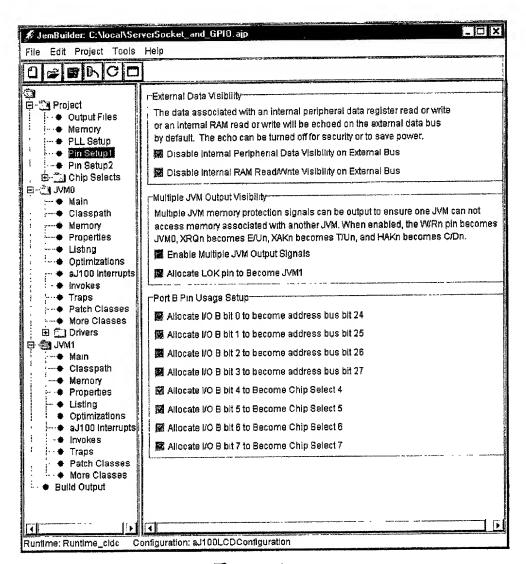


Fig. 15

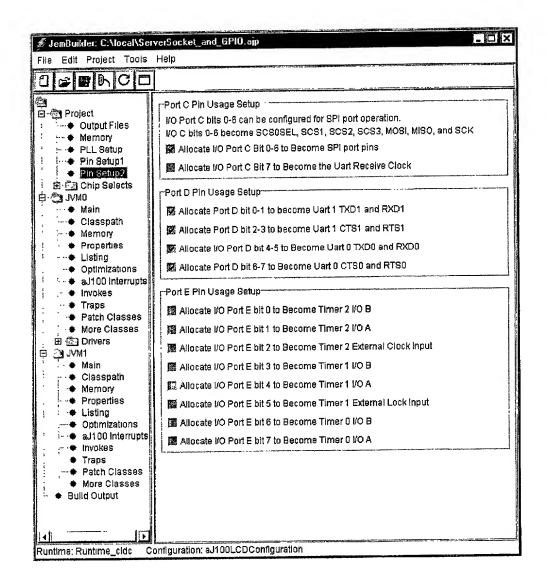


Fig. 16

Address Range This Chip Select Includes 4MB from 0x0 to 0x3FFFFF  Memory PLL Setup Pin Setup1 Pin Setup2 CS1 CS3 CS4 CS4 CS5 CS5 CS5 CS5 CS6 CS7 CS7 CS8 CS8 CS8 CS9	JemBuilder: C:\local\ServerSocket_and_GP10.aip
Address Range This Chip Select Includes 4MB from 0x0 to 0x3FFFFF  We Memory PLL Setup Prin Setup1 Pin Setup2 Chip Selects CS3 CS3 CS3 CS4 CS5 CS5 CS6 CS6 CS6 CS6 CS6 CS7 Walt States Between C9 Valid and WE or RE Optimizations Alton Classes More Classes	File Edit Project Tools Help
This Chip Select Includes 4MB from 0x0 to 0x3FFFFF  Memory PLL Setup Pin Setup1 Pin Setup2 Pin Setup2 Citip Selects Citip Select the Bus Width Citip Select the Bus	
Runtime: Runtime_cidc Configuration: aJ100LCDConfiguration	This Chip Select Includes 4MB from 0x0 to 0x3FFFFF  Memory PLL Setup Pin Setup1 Pin Setup2 Cip Selects Select CS2 CS3 CS4 CS4 CS5 CS6 CS6 CS6 CS6 CS6 CS7 CS7 CS7 CS7 CS8

Fig. 17 1700

₹ JemBuilder: C:\local\S	erverSocket_and_GPIO.ajp
File Edit Project Tools	Help
OPBRCC	
Project Output Files Memory PLL Setup Pln Setup1 Pln Setup2 Chip Selects JVM0 Classpath Memory Properties Listing Optimizations Invokes Traps Patch Classes More Classpath Memory Properties Listing Optimizations Invokes Traps Patch Classes Memory Properties Listing Nemory Properties Listing Nemory Properties Listing Nemory Properties Listing Properties Listing Nemory Properties Listing Optimizations AJ100 Interrupts Invokes Traps AJ100 Interrupts Nemory Properties Listing Optimizations AJ100 Interrupts Nemory Properties	Application Main Method  Fully Qualified Class Name of Main Method eg(com.ajile.example.MainClass):  Examples ServerSocket.ServerSocketExample  Fully qualified name of Main Method (default. main([Ljava/lang/String;)V):  Static Initialization  Fully Qualified Class Name to be Initialized First:  Additional Methods  Method Called When JVM is idle, Must have signature ()V:

Fig. 18

Application Classpath Classpath: Classpath: Classpath: Classpath Pin Setup1 Pin Setup2 Pin Chip Selects Dimony Properties Usting Optimizations Aut 00 Interrupts Divers Listing Optimizations Listing Optimizations Aut 00 Interrupts Classpath Classp	# JemBuilder: C:\local\Se	erverSocket_and_GPIO.ajp
Project  Output Files  Memory  Pin Setup  Pin Setup  Pin Setup2  Pin Chip Selects  Classpath  Memory  Main  Classpath  Classpath  Memory  Properties  Listing  Patch Classes  More Classes  More Classes  More Classpath  Main  Main  Main  Main  Main  Main  Main  More Classes  More Classes  More Classes  More Classes  Min Main  Main  Main  Main  Main  Main  Olasspath  Memory  Properties  Listing  Optimizations  Auailable Libraries:	OPBACE	
→ Invokes → Traps → Patch Classes → More Classes - ◆ Build Output	Droject Output Files Memory PLL Setup Pin Setup1 Pin Setup2 Chip Selects DVM0 Main Memory Properties Listing Optimizations Al 100 interrupts Invokes Traps Patch Classes More Classes Memory Properties Listing Optimizations Al 100 interrupts Listing Optimizations Invokes Traps Patch Classes Listing Optimizations Invokes Traps Al 100 interrupts Invokes Traps Patch Classes Memory Properties Listing Optimizations Al 100 interrupts Drivers Univers Univers Drivers	Classpath:  C:\ajile  Libraries:  Available Libraries:  <-  ->

Fig. 19

	rverSocket_and_GP10.ajp	
File Edit Project Tools		
		naka kipia ia. ian. ia, ia dagan anakan a
Project  Output Files  Memory  PLL Setup  Pin Setup2  Pin Setup2  Chip Selects  JVM0  Main  Classpath  Properties  Listing  Optimizations  AJ100 Interrupts  Invokes  Traps  Patch Classes  More Classes  Mann  Classpath  Memory  Properties  Listing  Optimizations  AJ100 Interrupts  Invokes  Traps  Patch Classes  More Classes  Hall Drivers  JVM1  Mann  Classpath  Memory  Properties  Listing  Optimizations  AJ100 Interrupts  Invokes  Traps  Patch Classes  More Classes  Build Output	UVM Placement Locate JVM In next available memory RAM (Data) Start Address: RAM (Data) Size (byles): ROM (Code) Start Address: ROM (Code) Size (byles):  Memory Allocations (byles) Initial Heap Size:  0x6000 Main Thread Stack Size: 0x6000 New Thread Stack Size: 0x400 Idle Thread Stack Size: 0x100 Executive Stack Size: 0x1000 Abort Stack Size: 0x1000 Thread Control Block Size: 0xb0	Default
	onfiguration: aJ100LCDConfiguration	

Fig. 20

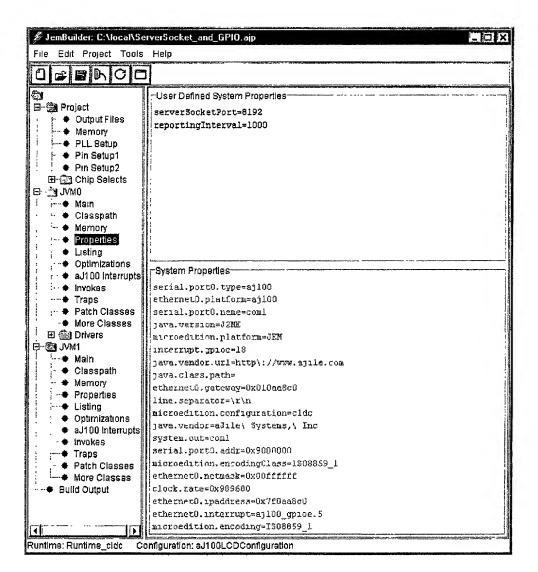


Fig. 21

File Edit Project Tools Help    Project   Proj	🖋 JemBuilder: C:\local\Se	rverSocket_and_GPIO.ajp	.□x
Project    Output Files   Mamory	File Edit Project Tools	Help	
Decided Project  Output Files  Memory  PIL Setup  Pin Setup1  Pin Setup2  Decided Chip Selects  Decided Chip			
Runtime: Runtime_cldc Configuration: aJ100LCDConfiguration	Project Output Files Mamory PLL Setup Pln Setup2 Chip Selects DyM0 Classpath Memory Properties Invokes Traps Patch Classes More Classes Memory Properties Invokes Traps Patch Classes Memory Properties Invokes Traps Patch Classes Memory Properties Isting Optimizations JUM1 Main Classpath Memory Properties Listing Optimizations JUM1 Main Classpath Memory Properties Listing Optimizations JUM1 Main Classpath Memory Properties Listing Optimizations JUM1 Properties Listing Optimizations Juvokes Invokes Nemory Properties Listing Optimizations Juvokes Build Output	List Options— List Filename (List):  List Data Memory List Initial Object Memory List Method Memory List Boot Strap Table List IDB Memory List Shared Classes GOrder Listing By Address 区 List Integer Bytes From Right To Left (reverse order)	

Fig. 22

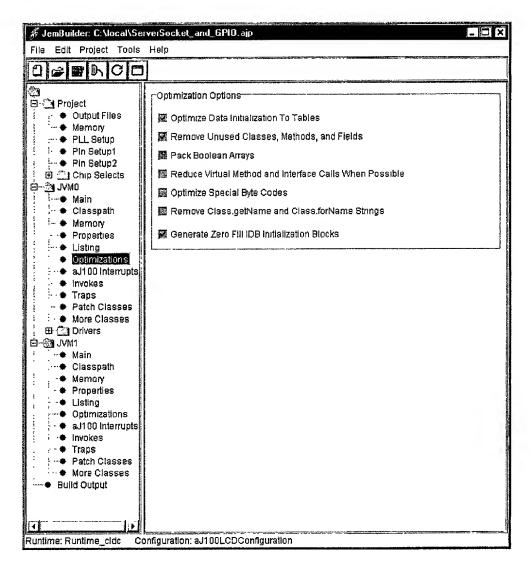


Fig. 23

	erverSocket_and_GPIO.ajp
File Edit Project Tools	Help
1 2 B D C C	<b>.</b>
Output Files  Memory PLL Setup Pin Setup1 Pin Setup2 Chip Selects Chip	Transfer Error XERR
	onfiguration; aJ100LCDConfiguration
2400	2406

Fig. 24

∮ JemBuilder: C:\local\Serv	
	TIOP
Divers  Propert  Dottout Files  Memony PIL Setup Pin Setup1 Pin Setup2 Chip Selects  JVM0 Main Classpath Memony Properties Listing Optimizations Jointerrupts Fraps Patch Classes More Classes Memony Properties Listing Optimizations Jointerrupts Fraps Patch Classes Memony Properties Listing Optimizations Jinvokes Traps Patch Classes More Classes More Classes More Classes	Fully Qualified Method Name Including Signature eg: com.ajile.Uart.handler(V) Note: Must be a static non-synchronized method multiArray(III)Ljava/lang/Object; Invoke 0: com.ajile.jem RTS_Memory.invokaMultiArray(Ljava/lang/Object; resolveClassRef(III)V Invoke 1: com.ajile.jem.RTS_DLink.invokeResolveClassRef(II)V resolveInstanceFieldRef(II)V Invoke 2: com.ajile.jem.RTS_DLink.invokeResolveInstanceFieldRef(II)V resolveClassFieldRef(II)V Invoke 3: com.ajile.jem.RTS_DLink.invokeResolveClassFieldRef(II)V resolveClassFieldRef(II)V Invoke 4: com.ajile.jem.RTS_DLink.invokeResolveInstanceMethodRef(II)V resolveClassMethodRef(II)V Invoke 5: com.ajile.jem.RTS_DLink.invokeResolveClassMethodRef(II)V resolveClassMethodRef(II)V Invoke 6: com.ajile.jem.RTS_DLink.invokeResolveClassMethodRef(II)V stackChunkCopy(III)V Invoke 6: com.ajile.jem.RTS_DLink.invokeResolveInstanceMethodRef(II)V stackChunkCopy(III)V Invoke 7: com aiile.jem.RTS_DLink.invokeResolveInterfaceMethodRef(II)V aThrow(II)Java/lang/Throwable;II)V Invoke 8: com.ajile.jem.RTS_Exception.invokeThrow(Djact(III.java/lang/Throwable;)V] 2500  2504
Runtime: Runtime cldc Cor	figuration: aJ100LCDConfiguration

Fig. 25

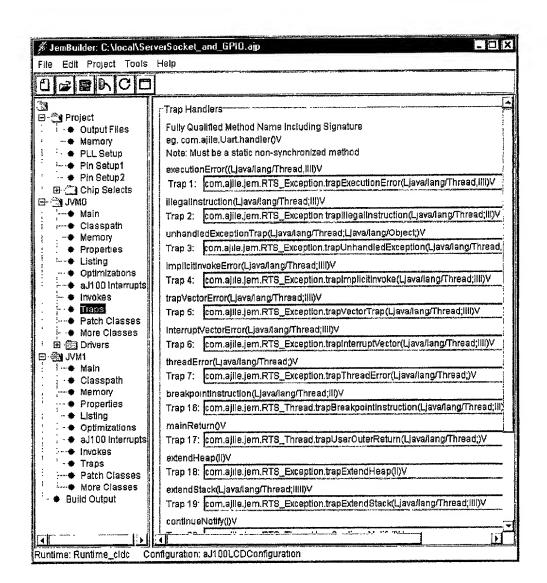


Fig. 26

	erverSocket_and_GP10.ajp	
File Edit Project Tools	: Help	
Project Output Files Memory PLL Setup Pln Setup1 Pin Setup2 Chip Selects JVM0 Main Classpath Memory Properties Listing Optimizations Invokes Traps More Classes Drivers JVM1 Main Classpath Memory Properties Listing Optimizations JVM1 Memory Properties Listing Optimizations JINO interrupts Invokes Traps Patch Classes More Classes More Classes Build Output		Remove Add  e Libranes:

Fig. 27

File Edit Project Tools Help  Project  Project  Project  Puly Qualified Classes In Link  Fully Qualified Class NameTo Add (eg com.ajile.Example.Test1):  Add  Remove  Pin Setup1 Pin Setup2 Pin Setup2 Pin Setup3 Properties Ustring Optimizations Add  Exclude Classes From Link  Fully Qualified Class Name To Exclude (eg com.ajile.Example.Omit):  Finvokes Patch Classes  More Classes Properties Ustring Optimizations Add  Remove  Remove
Project
Project  Output Files  Memory  PLL Setup  Pin Setup1  Pin Setup2  Chip Selects  Optimizations  Invokes  Traps  Patch Classes  Wine Classpath  Memory  Properties  Listing Optimizations  JWM1  Main  Classpath  Memory  Patch Classes  Listing Optimizations  Listing Optimizations  JWM1  Main  Classpath  Memory  Patch Classes  Listing Optimizations  JUM1  Main  Classpath  Memory  Properties  Listing Optimizations  Add  Remove
More Classes  Build Output

Fig. 28

JemBuilder: C:\local\Ser	
File Edit Project Tools	
Project Output Files Memory PLL Setup Pin Setup1 Pin Setup2 Din Setup3 Din Se	Ethernet Storage Choices  Ethernet IP Information can be stored as system properties, Or a memory location can be specified where the IP information can be found  Save IP Address as System Property  Specify Memory Where IP Information Can Be Found  Ethernet IP Information  IP Address  Net Mask  Gateway Address  Specify memory addresses to find IP information  Note. Must be on 4 byte boundry (Quad Byte Boundry)  IP Address Location:  Net Mask Location:  Ox4  Gateway Address Location  Ox4  Gateway Address Location  Ox4

Fig. 29

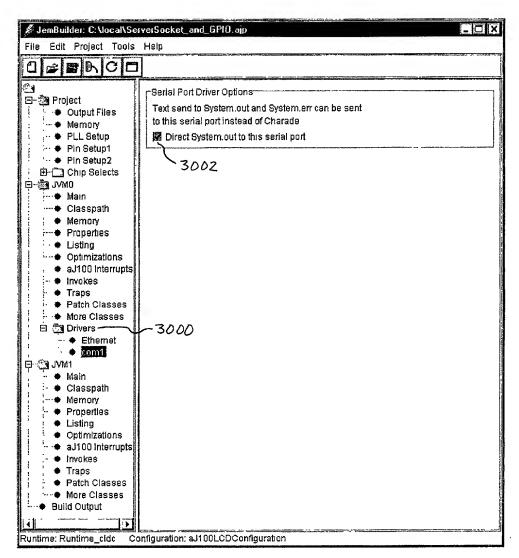


Fig. 30

💰 JemBuilder: C:\local\Se	erverSocket_and_GPIO.ajp	×
File Edit Project Tools	Help	none de la constante de la con
OPBRCE		
Project  Output Files  Memory  PLL Setup  Pin Setup1  Pin Setup2  Chip Selects  JVM0  Main  Classpath  Memory  Properties  Listing  Optimizations  AJ100 Interrupts  Invokes  Traps  Patch Classes  More Classes  Memory  Main  Classpath  Memory  Properties  Listing  Optimizations  Listing  Optimizations  Listing  Optimizations  Listing  Optimizations  Listing  Properties  Listing  Main  Classpath  Memory  Properties  Listing  Optimizations  Listing  Optimizations  AILOU Interrupts  Invokes  Traps  Patch Classes  More Classes  More Classes  More Classes  Build Output	Timer/Counter TCO	· <u>4</u>
	RQ 7:	
Runtime_cldc C	onfiguration; aJ100LCDConfiguration	<u> </u>
	3100	/
	Fig. 31	

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